

## TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

<b>Title of Invention</b>	Method and Apparatus for Creating Circuit Redundancy in Programmable Logic Devices		
Application Number :			
Date :			
First Named Applicant:	Praveen K Samudrala		
Confirmation Number:			
Attorney Docket Number:	1372.136.PRC		
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Submitted By:		Elec. Sign.	Sign. Capacity
Anton J Hopen Registered Number: 41849		/anton j hopen/	Attorney

Documents being submitted:	Files
us-request	1372_136_PRC-usrequ.xml us-request.dtd us-request.xsl
us-fee-sheet	1372_136_PRC-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
application-body	Specification-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd fig01.TIF fig02.TIF fig03.TIF fig04.TIF fig05.TIF fig06.TIF
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